



## NEWS RELEASE

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FOR IMMEDIATE RELEASE

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### **New Intel® 13<sup>th</sup> Gen Hybrid CPU Powered 3U OpenVPX™ Processing Engine is First SOSA Aligned SBC to Support Time-Sensitive Networking for Deterministic Ethernet**

*New Fabric100™ VPX3-1262 SBC features the 14-core Intel Raptor Lake Hybrid Processor to deliver more cores and higher performance than all previous Intel architecture based SBCs*

ASHBURN, Va. – October 4, 2023 – Curtiss-Wright's [Defense Solutions Division](#) today introduced the [VPX3-1262 single board computer](#) (SBC), the latest addition to its new [Fabric100™](#) family of extremely high-performance [SOSA™](#) aligned processing engines. The rugged 3U OpenVPX module, designed to meet the 3U I/O Intensive and Payload SOSA profiles, is powered by Intel's 13<sup>th</sup> Gen "Raptor Lake" 14-core hybrid architecture processor. The VPX3-1262 is the industry's first SOSA aligned plug-in-card (PIC) to support Time Sensitive Networking (TSN) on 1G and 10G Ethernet ports to provide deterministic Ethernet switching for time-critical data communications and complements Curtiss-Wright's simultaneously introduced [VPX3-6826 Ethernet switch](#), a new Fabric100 3U OpenVPX 100G Data Plane and Control Plane switch that also supports TSN. The VPX3-1262 SBC is ideal for use in general computing, mission processing, virtualized application hosting, [High Performance Embedded Computing \(HPEC\)](#) systems, and multi-SBC systems for advanced processing and ISR applications.

"Our Fabric100 family delivers a complete end-to-end solution for architecting 100Gbit SOSA aligned rugged systems," said Brian Perry, Senior Vice President and General Manager, Curtiss-Wright Defense Solutions Division. "The new 3U OpenVPX VPX3-1262 single board computer,

combined with our VPX3-6826 Ethernet switch, delivers a powerful performance optimized boardset that eliminates system bottlenecks for maximum performance.”

### **About Curtiss-Wright’s Fabric100 Suite of 3U and 6U OpenVPX Modules and Systems**

Fabric100 brings 100Gbit Ethernet and high-performance PCIe Gen4 interconnect speeds to tomorrow’s new generation of rugged deployable computing architectures. Today, system integrators struggle to satisfy their C5ISR applications’ insatiable appetite for sharing ever-increasing volumes of information. The higher-speed interconnects required to support these performance demands pose significant integration challenges for systems integrators. What’s more, the ability to meet the industry’s goal of simplified interoperability, in other words, to quickly and effectively build systems using open standards-based building blocks and make them work well together, becomes increasingly riskier as system designers migrate to faster 16Gbaud and 25Gbaud signaling technology and faster data throughput architectures. To address this daunting problem and reduce the system design risks associated with higher-speed interconnects, Curtiss-Wright has developed Fabric100, a complete end-to-end ecosystem of high-speed rugged OpenVPX modules and system components. It is not enough to simply provide 100G connections between a system’s modules yet fail to support the ability to process all this data within the modules themselves. Recognizing that, Curtiss-Wright’s Fabric100 board architectures are designed to deliver full 100G performance through the entire processing chain, eliminating data bottlenecks that might otherwise compromise system performance.

### **About the VPX3-1262 SBC**

Providing 25-40% more performance than Intel 9<sup>th</sup> Gen “Coffee Lake” processors and 75% more processing cores than the preceding 11<sup>th</sup> Gen “Tiger Lake” architecture, the VPX3-1262’s 14-core Raptor Lake hybrid processor increases processing efficiency by maintaining the same power requirements of the earlier devices. Intel’s first hybrid architecture embedded processor, the Raptor Lake features 14 cores and includes six (6) hyper-threading, high-performance P-cores (Performance) for complex workloads and eight (8) high-efficiency E-cores (Efficiency) offering significant power savings for less time-critical tasks. Both types of cores operate simultaneously and offer full instruction set compatibility. The processor is supported with up to 64 GB of high-speed, dual-channel DDR5 memory with ECC and up to 480 GB of onboard NVME storage for applications with demanding storage, data logging and sensor processing requirements.

System expansion and connectivity are provided by a 100G Ethernet Data Plane with RDMA/RoCEv2, PCI Express® (PCIe) Gen4 Expansion Plane, 10G Ethernet Control & Data Plane with TSN, and an XMC mezzanine site.

To [download the VPX3-1262 product sheet, please click here](#).

To [download the VPX3-6826 product sheet, please click here](#).

For information about availability of development boards and Quick Start Kits (QSKs) to support your program needs, please contact us at [ds@curtisswright.com](mailto:ds@curtisswright.com), visit our website at [www.curtisswrightds.com](http://www.curtisswrightds.com), or contact your local Curtiss-Wright sales representative.

For additional information about Curtiss-Wright [MOSA technologies](#), please visit [www.curtisswrightds.com](http://www.curtisswrightds.com), LinkedIn, and X [@CurtissWrightDS](#).

### **About Curtiss-Wright Corporation**

Curtiss-Wright Corporation (NYSE:CW) is a global integrated business that provides highly engineered products, solutions and services mainly to Aerospace & Defense markets, as well as critical technologies in demanding Commercial Power, Process and Industrial markets. We leverage a workforce of approximately 8,400 highly skilled employees who develop, design and build what we believe are the best engineered solutions to the markets we serve. Building on the heritage of Glenn Curtiss and the Wright brothers, Curtiss-Wright has a long tradition of providing innovative solutions through trusted customer relationships. For more information, visit [www.curtisswright.com](http://www.curtisswright.com).

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